



## Low-Voltage SOI CMOS VLSI Devices and Circuits (Hardback)

By James B. Kuo, Shih-Chia Lin

John Wiley and Sons Ltd, United States, 2001. Hardback. Book Condition: New. 242 x 163 mm. Language: English . Brand New Book. A practical, comprehensive survey of SOI CMOS devices and circuits for microelectronics engineers The microelectronics industry is becoming increasingly dependent on SOI CMOS VLSI devices and circuits. This book is the first to address this important topic with a practical focus on devices and circuits. It provides an up-to-date survey of the current knowledge regarding SOI device behaviors and describes state-of-the-art low-voltage CMOS VLSI analog and digital circuit techniques. Low-Voltage SOI CMOS VLSI Devices and Circuits covers the entire field, from basic concepts to the most advanced ideas. Topics include: SOI device behavior: fundamental and floating body effects, hot carrier effects, sensitivity, reliability, self-heating, breakdown, ESD, dual-gate devices, accumulation-mode devices, short channel effects, and narrow channel effects Low-voltage SOI digital circuits: floating body effects, DRAM, SRAM, static logic, dynamic logic, gate array, CPU, frequency divider, and DSP Low-voltage SOI analog circuits: op amps, filters, ADC/DAC, sigma-delta modulators, RF circuits, VCO, mixers, low-noise amplifiers, and high-temperature circuits With over 300 references to the state of the art and over 300 important figures on low-voltage SOI CMOS devices and circuits,...

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